AN7283S

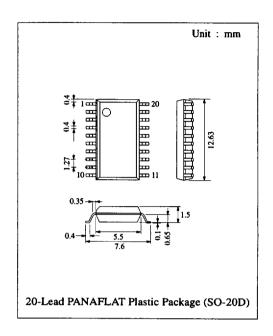
FM Front-end IC for Car Radio

■ Description

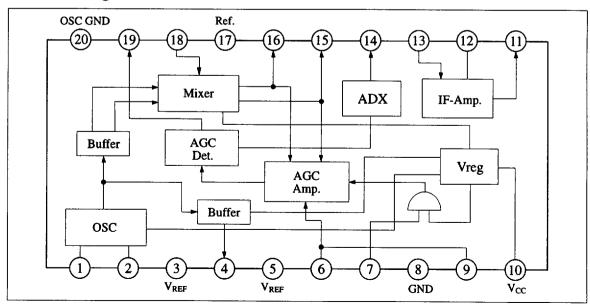
The AN7283S is a monolithic integrated circuit designed for car radio, supporting DTSs other than RF amp. It has buffer output of local oscillation frequency and also incorporates 2 Loop-AGC and PIN diode driver for antenna damping (ADX).

■ Features

- High sensitivity, high S/N ratio
- Improved IM characteristics at strong input
- 2 Loop-AGC supported
- PIN diode driver (ADX) built-in
- Built-in IF amp. with opposite (positive) temperature characteristics to RF amp.
- Difference from AN7280S
 - 1) One capacitor less at detection output
 - 2) IP6dB improved (122dBµ)
 - 3) Gain of pre-amp. fixed (25dB)
 - 4) S/N improved (+2dB)



Block Diagram



Panasonic

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Unit	
Supply Voltage	v _{cc}	9.6	V	
Supply Current	I _{CC}	48	mA	
Power Dissipation Ta = 75°C	P _D	230	mW	
Operating Ambient Temperature	Торг	-30 ~ +80	°C	
Storage Temperature	Tstg	-55 ~ +125	°C	

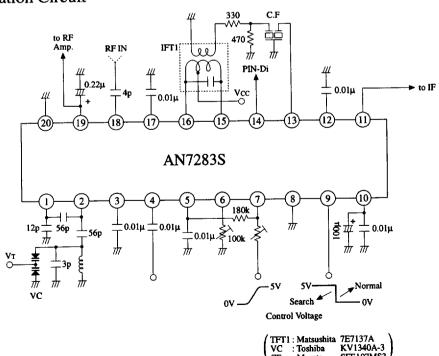
Operating Supply Voltage Range: $V_{CC} = 7.2V \sim 9.2V$

Electrical Characteristics (VCC=8V, Ta=25°C)

Item	Symbol	Condition	min.	typ.	max.	Unit
S/N Ratio	Nout	V _{IN} =17dBμ no modulation, however S is output at 400Hz, 30%modulation	22	30		dВ
Local Oscillation Output Level	Vosc	fosc=108.7MHz No signal input, measure at Pin 4	145	180	215	mV
IF Output Level	Vout	V _{IN} =65dBμ	43	60	85	mV
AGC Maximum Sensitivity	SAGCI	Input level for V _{AGC} =3V	60	63	66	dΒμ
AGC Sensitivity Variable Width	WAGC	Difference bet. input level for V _{AGC} =3V and S _{AGC1} .	37	40	43	dB
AGC Voltage (H)	V _{AGC(H)}	V _{IN} =58dBμ	6.0	6.4	6.8	V
AGC Voltage (L)	V _{AGC(L)}	V _{IN} =68dBμ		0.05	0.5	V

Note) For tuning, variable capacitance or f_{in} must be adjusted for maximum IF output level. (f_{in} =17dB μ)

Application Circuit



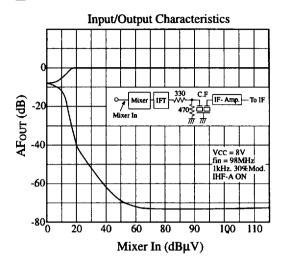
■ Pin Descriptions

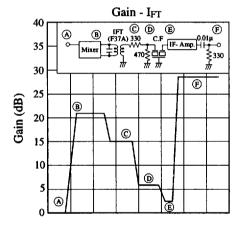
Pin No.	Pin Name	Equivalent Circuit	Description
1	OSC Emitter	○ V _{REF} (5V)	Local oscillation transistor emitter pin.
2	OSC Base	0 9	Local oscillation transistor base pin.
3 5 17	V _{REF} By-pass	sv Sv Sy	V _{REF} by-pass pin for mixer, OSC. buffer and OSC. section.
4	OSC Buffer Output	V _{REF} (5V) 4	Pin for output OSC signal to pre-scaler.
6	AGC-Amp. Gain Adjuster	6 5.6k	Pin for adjusting AGC Amp. gain by external resistor.
7	Control Signal Input	1.5k 7	Pin for adjusting AGC Amp. gain through input of control signal from IF section.
8	GND		
9	SSC	To AGC Amp.	Pin for adjusting AGC-Amp. gain through input of control signal from microcomputer.

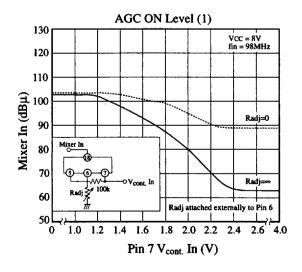
■ Pin Descriptions (Continue)

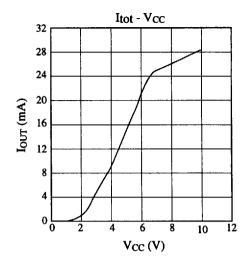
Pin No.	Pin Name	Equivalent Circuit	Description
10	V _{CC}		
11	IF-Amp. Output	$ \begin{array}{c c} \hline 240 \\ \hline 3k \\ \hline \end{array} $ $ \begin{array}{c c} \hline Zout = 330\Omega \end{array} $	IF Amp Output pin.
12	IF-Amp. By-pass	(330)	IF-Amp. by-pass pin.
13	IF-Amp. Input		IF-Amp. input pin.
14	ADX Output	• v _{cc}	PIN diode driver output pin. Determine maximum current to PIN diode by pin 14 external resistance value.
15	- Mixer Output	(5)	Mixer output pin.
16			
18	Mixer Input	(8)	Mixer input pin.
19	Level Detection Output	Д 23µА (19)	AGC signal output pin for second gate of RF Amp.
20	GND		OSC GND.

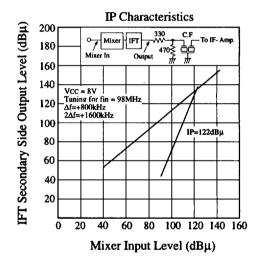
■ Characteristics Curve

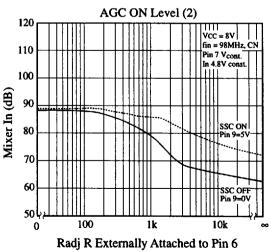




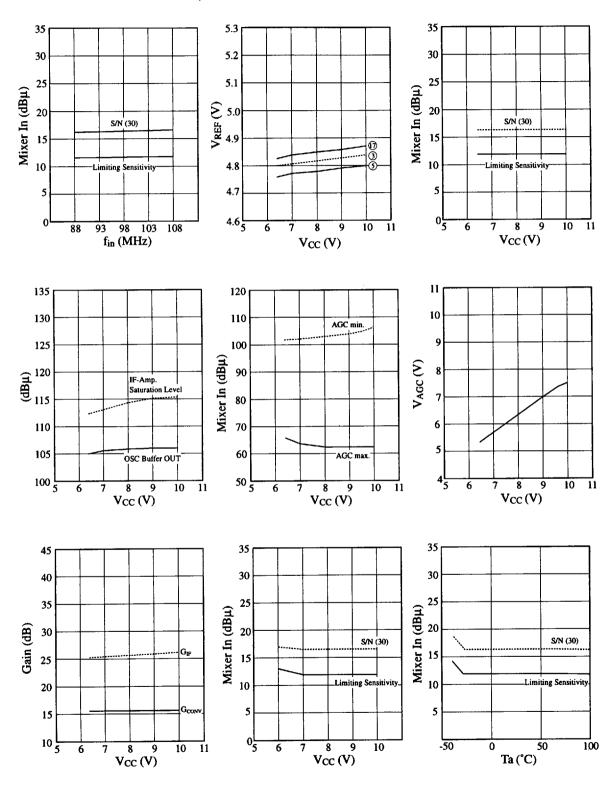








■ Characteristics Curve (Continue)



■ Characteristics Curve (Continue)

